



Application No. 10/782,782
Sec.1143
Response dated 26 January 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of :
Don-Woo LEE et al. : Group Art Unit 2891
Application No. 10/782,782 : Examiner Asok K. SARKAR
Filed 23 February 2004 :

METHOD OF FORMING A CONDUCTIVE PATTERN OF A SEMICONDUCTOR
DEVICE AND METHOD OF MANUFACTURING A NON-VOLATILE
SEMICONDUCTOR MEMORY DEVICE USING THE SAME

AMENDMENT AND/OR RESPONSE UNDER 37 C.F.R. § 1.112

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Alexandria, VA 22314

Sir:

In response to the Office Action of 3 October 2005, the period for response to which is hereby extended by the accompanying Petition and Petition Fee through 3 February 2006, reexamination and reconsideration are respectfully requested in view of the following Remarks.

Remarks begin on page 2 of this paper.